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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/646,317	08/22/2003	Esin Terzioglu	13454US03	9696	
23446	7590 02/23/2004		EXAM	EXAMINER	
	WS HELD & MALLO	HUR, Л	HUR, JUNG H		
500 WEST M SUITE 3400	IADISON STREET		ART UNIT	PAPER NUMBER	
CHICAGO,	IL 60661		2824		

DATE MAILED: 02/23/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

			N				
	Application No.	Applicant(s)	- CA				
	10/646,317	TERZIOGLU ET AL.					
Office Action Summary	Examiner	Art Unit	·				
	Jung (John) Hur	2824					
The MAILING DATE of this communication ap Period for Reply	opears on the cover sheet with the o	correspondence addre)SS				
A SHORTENED STATUTORY PERIOD FOR REP THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR 1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a re - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statu Any reply received by the Office later than three months after the maili earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be tingle the ply within the statutory minimum of thirty (30) day if will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONE.	mely filed ys will be considered timely. the mailing date of this comm ED (35 U.S.C. § 133).	nunication.				
Status							
1) Responsive to communication(s) filed on							
	is action is non-final.						
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4) ☐ Claim(s) 12-17 is/are pending in the application 4a) Of the above claim(s) is/are withdress. 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 12-17 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/	awn from consideration.						
Application Papers							
9) The specification is objected to by the Examin		As buther 5 sections					
10) The drawing(s) filed on 22 August 2003 is/are Applicant may not request that any objection to the							
Replacement drawing sheet(s) including the corre	- · ·		1.121(d).				
11) The oath or declaration is objected to by the E		•	` '				
Priority under 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreig a) All b) Some * c) None of: 1. Certified copies of the priority documer 2. Certified copies of the priority documer 3. Copies of the certified copies of the priority application from the International Burea * See the attached detailed Office action for a list	nts have been received. nts have been received in Applicat ority documents have been receive au (PCT Rule 17.2(a)).	ion No ed in this National Sta	age				
Attachment(s)) Notice of References Cited (PTO-892) Description Notice of Draftsperson's Patent Drawing Review (PTO-948)	4)						
(r) Notice of Draitsperson's Falent Drawing Review (r 10-946) (r) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date 11/28/03.		Patent Application (PTO-15	2)				

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DETAILED ACTION

Preliminary Amendment

Acknowledgment is made of applicant's Preliminary Amendment, filed 22
 August 2003. The changes and remarks disclosed therein were considered.

Claims 1-11 have been cancelled, and claims 12-17 have been added. Therefore, claims 12-17 are pending in the application.

Information Disclosure Statement

2. Acknowledgment is made of applicant's Information Disclosure Statement (IDS) Form PTO-1449, filed 28 November 2003. The information disclosed therein was considered.

Specification

3. The disclosure is objected to because of the following informalities:

The status of U.S. Application No. 09/776,029 recited on page 1, paragraph 1 of the amended specification should be updated. Said Application has matured into U.S. Pat. No. 6,611,465.

Appropriate correction is required.

Double Patenting

4. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*,

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422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

5. Claims 12-17 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-6 of U.S. Patent No. 6,611,465 (Reference). Although the conflicting claims are not identical, they are not patentably distinct from each other because:

The method of implementing a diffusion replica delay circuit as claimed in claims 12-17 of instant Application is directly associated with the diffusion replica delay circuit claimed in claims 1-6 of Reference.

Claims 1-6 of Reference claim a diffusion replica delay circuit comprising a diffusion replica capacitor and a diffusion replica transistor in a memory component including a dummy cell with a dummy bit line, as claimed in claims 12-17 of instant Application, with the exception of a method of implementing said diffusion replica delay circuit. In view of claims 1-6 of Reference, it would have been obvious at the time the invention was made to a person having ordinary skill in the art to come up with a method of implementing a diffusion replica delay circuit, as claimed in claims 12-17 of instant Application, since discovering a method directly associated with the diffusion replica delay circuit as claimed in claims 1-6 of Reference involves only routine skill in the art.

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Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jung (John) Hur whose telephone number is (571) 272-1870. The examiner can normally be reached on M-F 6:30 AM - 3:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

jhh

MICHAEL S. LEBENTRITT PRIMARY EXAMINER